

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No. <u>10/816,727</u>	
				M-15372 US		Unassigned	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)			
(Use several sheets if necessary)				Kook-Hwan Kwon			
				Filing Date		Group	
				Filed Herewith		Unassigned <u>2824</u>	
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<u>TL</u>	AA	6,011,737	4 Jan. 2000	Li et al.	—	—	
<u>TL</u>	AB	6,115,321	5 Sept. 2000	Koelling et al.	—	—	
<u>TL</u>	AC	6,285,578	4 Sept. 2001	Huang	—	—	
<u>TL</u>	AD	6,563,747	13 May 2003	Faue	—	—	
<u>TL</u>	AE	6,597,630	22 July 2003	Kubo et al.	—	—	
<u>TL</u>	AF	6,600,691	29 July 2003	Morzano et al.	—	—	
<u>TL</u>	AG	6,621,747	16 Sept. 2003	Faue	—	—	
<u>TL</u>	AH	2002/0149960	17 Oct. 2002	Yoo et al.	—	—	
<u>TL</u>	AI	2003/0026161	6 Feb. 2003	Yamaguchi et al.	—	—	
	AJ						
	AK						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
<u>TL</u>	AL	JEDEC Standard: DDR2 SDRAM Specification: JESD79-2A, January 2004, JEDEC Solid State Technology Association.					
<u>TL</u>	AM	"Preliminary Data Sheet: 1G bits DDR2 SDRAM: EDE1104AASE (256M words x 4 bits); EDE1108AASE (128M words x 8bits); Elpida Memory, Inc. 2003.					
<u>TL</u>	AN	Davis, Brian; Mudge, Trevor; Jacob, Bruce; Cuppu, Vinodh "DDR2 and Low Latency Variants" Electrical Engineering and Computer Science, University of Michigan, Ann Arbor; Electrical & Computer Engineering, University of Maryland, College Park, pages 1-15.					
<u>TL</u>	AO	HYS64T32000GDL (256 Mbyte); HYS64R64020GDL (512 Mbyte) DDR2 Small Outline DIMM Modules; Data Sheet, Vol. 82, Oct. 2003, Infineon Technologies.					
<u>TL</u>	AP	"Preliminary Data Sheet: 512 M bits DDR SDRAM: EDD5104ABTA (128M words x 4 bits); EDD5108ABTA (64M words x 8 bits); Elpida Memory, Inc. 2002-2003.					
<u>TL</u>	AQ	JEDEC Standard: Double Data Rate (DDR) SDRAM Specification: JESD79D, January 2004, JEDEC Solid State Technology Association.					
<u>TL</u>	AR	United States Patent Application No. 10/794,782, entitled "Data Sorting in Memories," Filed on March 3, 2004; Attorney Docket No.: M-15341 US.					
Examiner		<u>TOAN CE</u>		Date Considered		<u>12/16/2005</u>	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							